

應用科學研究中心

Research Center for Applied Sciences



Prof. Ya-Lun HO 何亞倫 教授

Assistant Professor
Dept. of Mechanical Engineering, The
University of Tokyo, Japan

**Top-down fabrication and plasmonic
structure design for on-chip nanolasers**

Date: 2019.11.19 (Tue.)

Time: 14:00-15:00

**Venue: B106 Auditorium, 1F, IRBST,
Academia Sinica**

跨領域科技研究大樓 1F B106 演講廳

Host: Prof. Chang, Yun-Chorng 張允崇研究員

Contact: Ms. Yi-Yun Wang
Email: as0190606@gate.sinica.edu.tw
Tel: 02-2787-3108